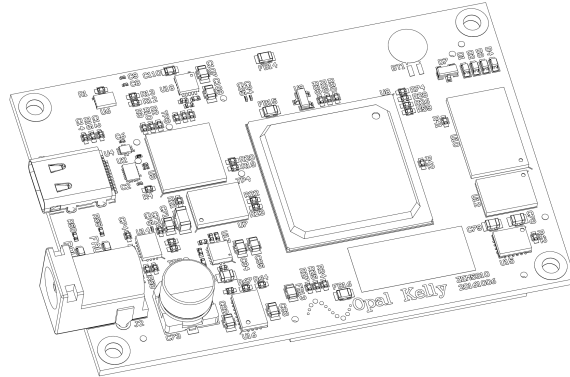


# ZEM5310

The ZEM5310 is a compact USB 3.0 (SuperSpeed) FPGA integration module featuring the Altera Cyclone V FPGA, 4 Gib (256 M x 16-bit) DDR3 SDRAM, two 128 Mib SPI Flash devices, high-efficiency switching power supplies, and two high-density 0.8mm expansion connectors. The USB 3.0 SuperSpeed interface provides fast configuration downloads and PC-FPGA communication as well as easy communication with our popular FrontPanel application and SDK. A low-jitter 100 MHz oscillator attached to the FPGA provides a reliable clock source.



## Documentation and Reference Materials

The following is a comprehensive list of documentation available for this device.

<a href="#">ZEM5310 User's Manual</a>	This online documentation space.
<a href="#">ZEM5310 Pins Reference</a>	The interactive Pins reference for the device provides detailed pinout information as well as automated constraint file generation. Export functionality is provided to PDF, CSV. Constraint files are provided for UCF, XCD (Xilinx) and QSF, SDF (Altera).
<a href="#">FrontPanel SDK User's Manual</a>	The online documentation space for the FrontPanel SDK.
<a href="#">FrontPanel API Reference</a>	Online API reference with detailed usage for every API method.

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