

BRK7010 Breakout Board



Important Lifecycle Information

Unlike our integration modules, breakout boards are not intended for production integration. We reserve the right to change dimensions and functionality of this board at any time and may not necessarily have the previous version available for purchase.

The BRK7010 is a simple two-layer "breakout board" which can be used to evaluate or transition to the XEM7310. It provides standard 2-mm thru-hole connections to the 0.8-mm high-density connectors on the XEM7310 and a DC power connector (2.1mm / 5.5mm, center positive) for providing +5VDC to the XEM7310.

Please visit the [Pins reference for the XEM7310](#) for pin mapping details. Full Altium schematics and PCB layout are available through [Pins Downloads](#).

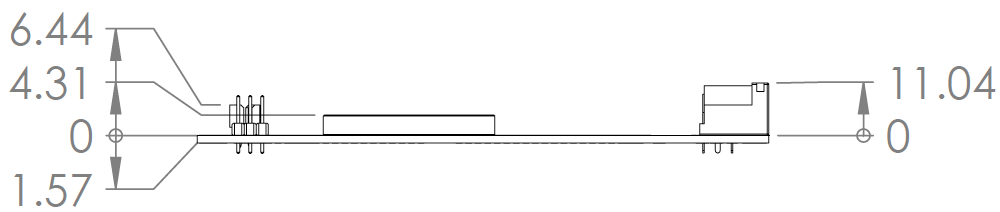
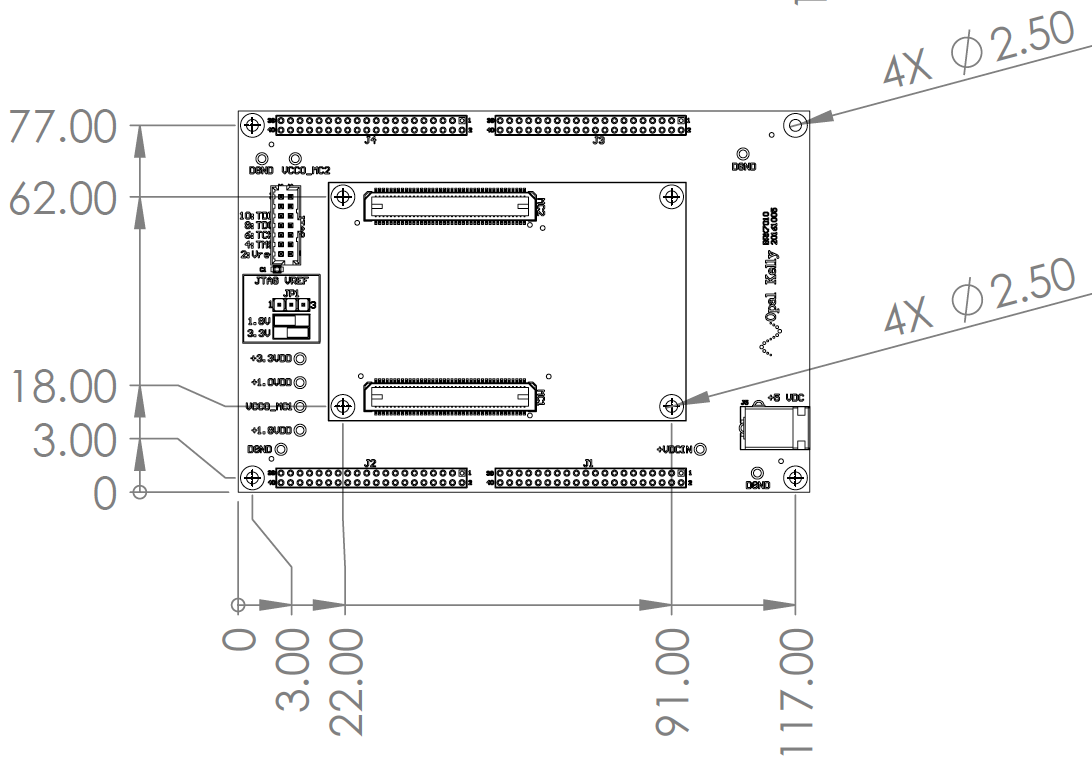
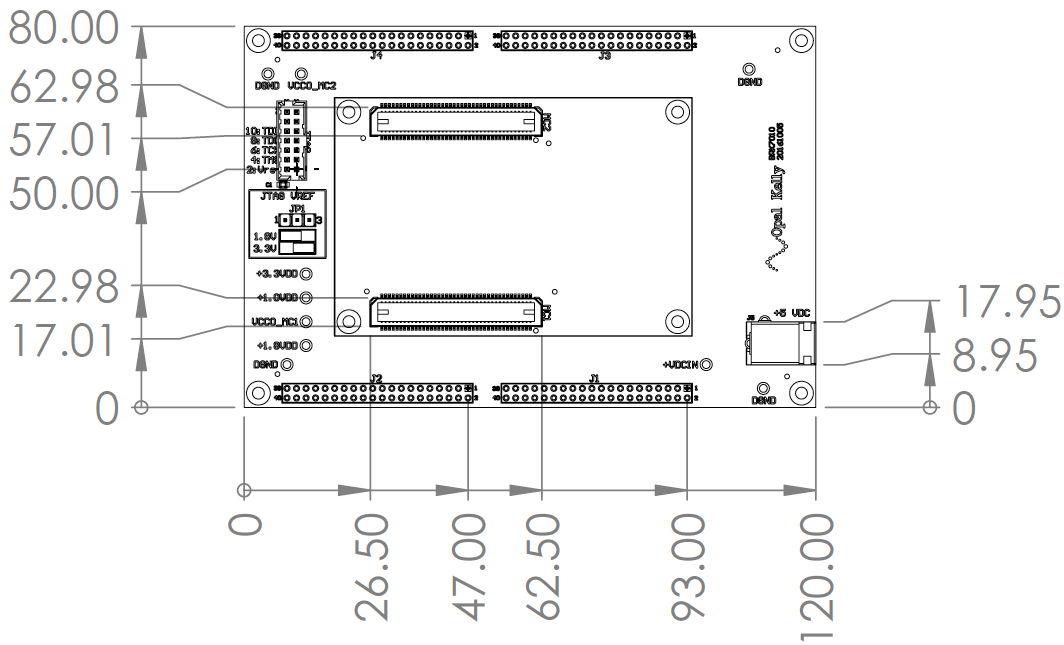
Xilinx JTAG Operation

The BRK7010 passes the JTAG interface from the expansion connectors on the XEM7310 to a connector compatible with the Xilinx JTAG cable. The reference voltage of this interface needs to be compatible with the XEM7310. Selection of this is done using a shunt on JP1 according to the table below.

FPGA Product	JTAG Reference Voltage	Shunt Position
XEM7010	3.3 V	Pins 2 - 3
XEM7310	1.8 V	Pins 1 - 2

Mechanical Drawing

[BRK7010-MechanicalDrawing.PDF](#)



All dimensions in millimeters.