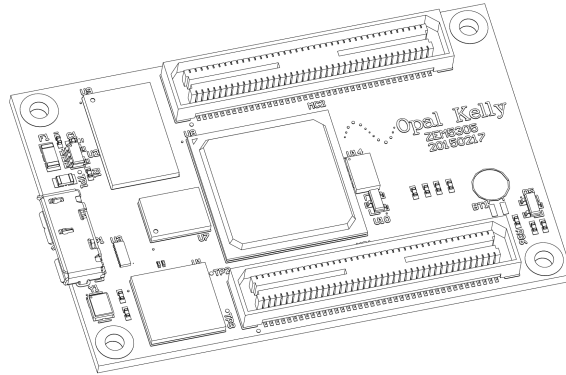


ZEM5305

The ZEM5305 is a compact USB 3.0 FPGA integration module featuring the Altera Cyclone V E FPGA, 512 MiB of 16-bit wide DDR3L memory, 32 MiB System Flash for self-boot capability, and two high-density 0.8-mm expansion connectors.

The USB 3.0 interface provides fast configuration downloads, FPGA-PC communication at USB 3.0 SuperSpeed rates, and easy access with our FrontPanel SDK. An on-board low-jitter clock oscillator provides a reference clock to the on-chip PLLs and clock management resources.



Documentation and Reference Materials

The following is a comprehensive list of documentation available for this device.

ZEM5305 User's Manual	This online documentation space.
ZEM5305 Pins Reference	The interactive Pins reference for the device provides detailed pinout information as well as automated constraint file generation. Export functionality is provided to PDF, CSV. Constraint files are provided for UCF, XCD (Xilinx) and QSF, SDF (Altera).
FrontPanel SDK User's Manual	The online documentation space for the FrontPanel SDK.
FrontPanel API Reference	Online API reference with detailed usage for every API method.

Copyright

Software, documentation, samples, and related materials are Copyright © 2006-2018 Opal Kelly Incorporated.

Opal Kelly Incorporated
Portland, OR
<https://www.opalkelly.com>

All rights reserved. Unauthorized duplication, in whole or part, of this document by any means except for brief excerpts in published reviews is prohibited without the express written permission of Opal Kelly Incorporated.

Opal Kelly® and FrontPanel® are registered trademarks of Opal Kelly Incorporated. Linux is a registered trademark of Linus Torvalds. Microsoft and Windows are both registered trademarks of Microsoft Corporation. All other trademarks referenced herein are the property of their respective owners and no trademark rights are claimed.